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Docket No. END920030065US1 (IEN-10-5782)				
Applicant(s):	Yu et al			·
Patent No. 6.984,997 B2				
Issue Date: January 10, 2006				
Serial No.	Filing Date		Examiner	Group Art Unit
10/714,215	November 13, 20	<u>103</u>	V. P. Nguyen	<u>2551</u>
Invention:				
METHOD AND SYSTEM FOR TESTING MULTI-CHIP INTEGRATED CIRCUIT MODULES				
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Fax. No. <u>571-</u>	<u>273-8300</u> on	MARCH 2- (Date)	4, 2006	(No. of pages)
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US 6,984,997 B2

Issue Date

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Application Number

10/714,215

Filing Date

November 13, 2003

First Named Inventor

**Attorney Docket** 

Yu

END920030065US1 (IEN-10-5782)

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